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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/678,375	10/03/2003	Kuei-Ann Wen	N0113/PP/HH	4031
41744	7590	11/15/2005	EXAMINER ROSSOSHEK, YELENA	
TRANSPACIFIC LAW GROUP 617 NORTH DELAWARE STREET SAN MATEO, CA 94401			ART UNIT 2825	

DATE MAILED: 11/15/2005 .

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/678,375

Applicant(s)

WEN, KUEI-ANN

Examiner

Helen Rossoshek

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 October 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 03 October 2003 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

1. This office action is in response to the Application 10/678,375 filed 10/03/2003.

2. Claims 1-20 are pending in the Application.

Drawings

3. The drawings are objected to because

Drawing 2 needs to be modified by adding arrows on the lines connecting the boxes in the diagram of the modularized circuit design information generating tool

Drawing 3 had typographical error in the box 104

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner,

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the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

4. The abstract of the disclosure is objected to because the abstract exceeds 150 words and has improper phraseology. Correction is required. See MPEP § 608.01(b).

Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to **150 words**. It is important that the abstract not exceed **150 words** in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "**said**" should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

Claim Objections

5. Claims 3-9, 12-18 are objected to because of the following informalities:

claim 3 line 3 after first "information" delete "file" insert --files--

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claim 3 line 3 after second "information" delete "file" insert --files--

claim 4 line 2 after "information" delete "file" insert --files--

claim 5 line 2 after "information" insert --files--

claim 6 line 2 after "information" insert --files--

claim 7 line 2 after "information" insert --files--

claim 8 line 2 after "information" insert --files--

claim 9 line 2 after "information" insert --files--

claim 12 line 3 after "information" delete "file" insert --files--

claim 12 line 4 after "information" delete "file" insert --files--

claim 12 line 5 after "information" delete "file" insert --files--

claim 13 line 2 after "information" delete "file" insert --files--

claim 14 line 2 after "information" insert --files--

claim 15 line 2 after "information" insert --files--

claim 16 line 2 after "information" insert --files--

claim 17 line 2 after "information" insert --files--

claim 18 line 2 after "information" insert --files--

Appropriate correction is required.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the

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applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

7. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Brown (US Patent Application Publication 20020156757).

With respect to claim 1 Brown teaches a modularized circuit design information generating tool a product design system (abstract), comprising a circuit module design database including circuit design information of functional modules of at least two categories within vendor database 40 shown on the Fig. 1 (paragraph [0046]), wherein at least one category of the functional modules includes design information of circuit modules of at least two different specifications within vendor databases 42, 44, 46, 48 representing different vendors (categories), wherein each vendor database contain plurality of files storing a vendor-supplied data, such as a specific material, component, product, model, design rule etc. (specifications) (paragraph [0047]); an element selection means allowing user to select suited circuit modules from the circuit module design database according to particular specifications of functional elements to be included into circuit to be designed and to include circuit design information corresponding to the selected circuit modules into circuit design information file of the circuit to be designed within selecting one or more of sets of data results from database 40 (paragraph [0023]), wherein the information of the selected components is accompanied by the information of material, process and the vendor name (paragraph [0052]); a circuit module connection means to define connections between or among selected circuit modules according to features of each selected circuit module within the

step of the electronic design system such as assembling the selected components using CAD design tools (paragraph [0090]); a memory to store circuit design information of all selected circuit modules and information of connections between and/or among the selected circuit modules, both of circuit under design or circuit as designed within creating the design file or collection of design files storing selected design information (paragraph [0084]); and a file converting means to convert circuit design information so obtained into an applicable format within translating design file into any format depending on the tool, which will be using the data of the design file (paragraphs [0023], [0030]).

With respect to claims 2-9 Brown teaches:

Claims 2, 11: wherein the circuit module design information database comprises a communications tool connectable to a remote database by using network 30 as shown on the fig. 1 (paragraph [0046]);

Claims 3, 12: wherein the circuit design information database comprises a group of circuit design information files for central processing unit, a group of circuit design information files for processing element, a group of design information files for memory circuit and a group of circuit design information files for interfacing circuit within the product design system that enables the designer to access data at all levels of the supply chain (paragraph [0014]), to search and retrieve any combination of data fields in the vendor database (paragraph 0074)), wherein the database holds the data of variety of components and devices (paragraph [0116]);

Claims 4, 13: wherein the group of circuit design information files for central processing unit comprises core circuit design information for at least two central processing units different in operational speed, length in instruction or bus width with each other within the product design system that enables the designer to access data at all levels of the supply chain (paragraph [0014]), to search and retrieve any combination of data fields in the vendor database (paragraph 0074)), wherein the database holds the data of variety of components and devices (paragraph [0116]);

Claims 5, 14: wherein the group of circuit design information files for processing circuit comprises circuit design information for at least two processing elements different in function.

Claims 6, 15: wherein the group of circuit design information files for processing circuit comprises circuit design information for at least one codec, at least one filter and at least one modulator within the product design system that enables the designer to access data at all levels of the supply chain (paragraph [0014]), to search and retrieve any combination of data fields in the vendor database (paragraph 0074)), wherein the database holds the data of variety of components and devices (paragraph [0116]);

Claims 7, 16: wherein the group of circuit design information files for memory circuit comprises circuit design information for at least two types of memory different in memory space within the product design system that enables the designer to access data at all levels of the supply chain (paragraph [0014]), to search and retrieve any combination of data fields in the vendor database (paragraph 0074)), wherein the database holds the data of variety of components and devices (paragraph [0116]);

Claims 8, 17: wherein the group of circuit design information files for interfacing circuit comprises circuit design information for at least two interfacing circuits different in function within the product design system that enables the designer to access data at all levels of the supply chain (paragraph [0014]), to search and retrieve any combination of data fields in the vendor database (paragraph 0074]), wherein the database holds the data of variety of components and devices (paragraph [0116]);

Claims 9, 18: wherein the group of circuit design information files for interfacing circuit comprises circuit design information for at least one A/D converter, at least D/A converter, a USB interface circuit and a PCMCIA interface circuit within the product design system that enables the designer to access data at all levels of the supply chain (paragraph [0014]), to search and retrieve any combination of data fields in the vendor database (paragraph 0074]), wherein the database holds the data of variety of components and devices (paragraph [0116]);

Claim 19: a memory stored with circuit design information generated from the method of any of claims 10-18 within storing retrieved information for the circuit design in the design files or optionally in the registry database 34 as shown on the Fig. 1 (abstract, paragraph [0032]);

Claim 20: a circuit prepared with circuit design information generated from the method of any of claims 10-18 within electronic product design system for designing electronic circuits using design tools, such as EDA, CAD etc. (paragraphs [0015], [0030]).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Helen Rossoshek whose telephone number is 571-272-1905. The examiner can normally be reached on 7:00-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew S. Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner
Helen Rossoshek
AU 2825


LEIGH M. GARBOWSKI
PRIMARY EXAMINER